



S/N 10/646,478
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Applicant: Daisuke Kawagoe

Examiner: Ishwar B. Patel

Serial No.: 10/646,478

Group Art Unit: 2841

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Docket No.: 884.937US1

Title: STACKED VIA STRUCTURE THAT INCLUDES
A SKIP VIA

Assignee: Intel Corporation

Customer Number: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

This paper responds to the Office Action mailed on November 15, 2005. Please amend the above-identified patent application as follows.